

华芯微特MCU(ARM Cortex-M0)32位微控制器产品列表																												
Series	Part Number	Supply Voltage(V)	Max Frequency (MHz)	Memory (KBytes)		I/O	Timer					Digital				Connectivity								Analog Interface			Package	Chip Size
				Flash	SRAM		Timer Supe	Timer Base	SysClk	WDT	RTC	PWM ³	Cordic	DIV	QEI	UART	I2C	SPI	I2S	CAN	SLCD	LED	I80	SIGMA DELTA ADC	SAR ADC	OP CMP		
SWM181	SWM181DBU6-40	2.3~3.6	48	120	16	30	4	1	1	1	0	8	1	1	0	4	2	2	0	1	1(4*13)	0	0	1(5)	1(7)	0/3	QFN40	5.0*5.0mm,e=0.4
	SWM181C9T6-50	2.3~3.6	48	96	16	38	4	1	1	1	0	8	1	1	0	4	2	2	0	1	1(4*22)	0	0	1(4)	1(7)	0/3	LQFP48	7.0*7.0mm,e=0.5
	SWM181CBT6-50	2.3~3.6	48	120	16	38	4	1	1	1	0	8	1	1	0	4	2	2	0	1	1(4*22)	0	0	1(4)	1(7)	0/3	LQFP48	7.0*7.0mm,e=0.5
	SWM181RCT6-50	2.7~3.6	48	248	16	50	4	1	1	1	0	8	1	1	0	4	2	2	0	1	1(4*31)	0	0	1(6)	1(8)	0/3	LQFP64	10.0*10.0mm,e=0.5
SWM241	SWM241KBT7-80	2.5~5.5	48	128	8	28	8	1	1	1	1	12	1	1	0	4	2	2	0	1	1(3*16)	1(8*13)	0	0	1(8)	0/0	LQFP32	7.0*7.0mm,e=0.8
	SWM241PBT7-80	2.5~5.5	48	128	8	40	8	1	1	1	1	15	1	1	0	4	2	2	0	1	1(4*30)	1(4*19)	0	0	1(12)	0/0	LQFP44	10.0*10.0mm,e=0.8

Series	Part Number	Supply Voltage(V)	Driver Voltage(V)	LDO	Gate Driver	Max Freq ncy (MHz)	Memory (KBytes)		I/O	Timer				Digital				Connectivity					Analog Interface		Package	Chip Size
							Flash	SRAM		Super	Base	SysClk	WDT	PWM ³	Cordic	DIV	QEI	UART	I2C	SPI	CAN	MPU	SAR ADC	OP CMP		
SWM211	SWM211C8T7-50	2.5~5.5	0	0	0	90	64	8	43	3	4	1	1	8	1	1	1	2	1	2	1	1	1(12)	4/4	LQFP48	7.0*7.0mm,e=0.5
	SWM211G6S7-65	2.5~5.5	0	0	0	90	32	8	24	3	4	1	1	8	1	1	1	2	1	1	1	0	1(11)	3/4	SSOP28	10.20*5.30mm,e=0.65
SWM221	SWM221CBT7-50	2.2~5.5	0	0	0	72	128	8	36	3	4	1	1	8	0	1	1	3 ⁵	1	2 ⁶	1	1	2(15) ⁷	3/2	LQFP48	7.0*7.0mm,e=0.5
	SWM221EBS7-63	2.2~5.5	0	0	0	72	128	8	20	3	4	1	1	8	0	1	1	3 ⁵	1	2 ⁶	1	0	2(10) ⁷	3/2	SSOP24	8.65*3.90mm, e=0.635
	SWM221KBU7-50	2.2~5.5	0	0	0	72	128	8	27	3	4	1	1	8	0	1	1	3 ⁵	1	2 ⁶	1	0	2(11) ⁷	3/2	QFN32	5.0*5.0mm,e=0.5
	SWM221DBU7-40	2.2~5.5	0	0	0	72	128	8	37	3	4	1	1	8	0	1	1	3 ⁵	1	2 ⁶	1	1	2(14) ⁷	3/2	QFN40	5.0*5.0mm,e=0.4
	SWM221GBS7-65	2.2~5.5	0	0	0	72	128	8	24	3	3	1	1	8	0	1	1	2 ⁵	1	2 ⁶	1	0	2(8) ⁷	3/2	SSOP28	10.20*5.30mm,e=0.65
	SWM22PE8S7-63	2.2~5.5	6-36	5v	3P3N	72	64	8	13	3	4	1	1	8	0	1	1	2 ⁵	1	2 ⁶	1	0	2(9) ⁷	3/2	SSOP24	8.65*3.90mm, e=0.635
	SWM22PG8S7-65	2.2~5.5	6-36	5v	3P3N	72	64	8	15	3	4	1	1	8	0	1	1	3 ⁵	1	1 ⁶	1	0	2(9) ⁷	3/2	SSOP28	10.20*5.30mm,e=0.65
	SWM22DD8U7-40	2.2~5.5	20(max)	5v	6N	72	64	8	21	3	4	1	1	8	0	1	1	3 ⁵	1	2 ⁶	1	0	2(11) ⁷	3/2	QFN40	5.0*5.0mm,e=0.4
	SWM22DK8U7-40	2.2~5.5	15(max)	5v	6N	72	64	8	12	2	3	1	1	6	0	1	1	3 ⁵	1	2 ⁶	0	0	2(8) ⁷	2/2	QFN32	4.9*4.9mm,e=0.5
SWM231	SWM231E6S7-63	2.2~5.5	0	0	0	60	32	4	22	1	3	1	1	8	0	1	0	2	0	1	0	0	1(15) ⁷	2/2	SSOP24	8.65*3.9mm,e=0.635
	SWM231F6U7-40	2.2~5.5	0	0	0	60	32	4	19	1	2	1	1	8	0	1	0	2	0	1	0	0	1(9) ⁷	2/2	QFN20	3.0*3.0mm,e=0.4
	SWM23PE6S7-63	2.2~5.5	6-36	5V	3P3N	60	32	4	14	1	3	1	1	8	0	1	0	2	0	0	0	0	1(11) ⁷	2/2	SSOP24	8.65*3.90mm,e=0.635
	SWM23DK6U7-40	2.2~5.5	15(max)	5V	6N	60	32	4	13	1	3	1	1	8	0	1	0	2	0	1	0	0	1(8)8	2/2	QFN32	4.9*4.9mm,e=0.5

华芯微特MCU(ARM Cortex-M4/安谋科技“星辰” STAR-MC1内核)32位微控制器产品列表																														
Series	Part Number	Supply Voltage(V)	Max Frequency (MHz)	Memory(KBytes)		Stacked SDRAM (Mbytes)	I/O	Timer					Digital				Connectivity								Analog Interface			Package	Chip Size	
				Flash	SRAM			Super	Base	Sys Clk	WDT	RTC	PWM	Cordic	DIV	CRC	UART	I2C	SPI	I2S	CAN	TFT LCD	SDIO	SFC	USB	SAR ADC	DAC			OP CMP
SWM341	SWM341CET7-50	3.0~3.6	150	512	64	0	37	5	10	1	1	1	12	1	1	1	4	2	2	2	2	1	0	1	1	2(11)	1	1/3 ¹	LQFP48	7.0*7.0mm,e=0.5
	SWM341RET7-50	3.0~3.6	150	512	64	0	51	5	11	1	1	1	18	1	1	1	4	2	2	2	2	1	1	1	1	2(14)	1	3/1	LQFP64	10.0*10.0mm,e=0.5
	SWM341VET7-50	3.0~3.6	150	512	64	0	72	5	12	1	1	1	20	1	1	1	4	2	2	2	2	1	1	1	1	2(15)	1	4/3	LQFP100	14.0*14.0mm,e=0.5
	SWM34SCET6-50	3.0~3.6	120	512	64	2	37	5	10	1	1	1	12	1	1	1	4	2	2	2	2	1	0	1	1	2(11)	1	1/3 ¹	LQFP48	7.0*7.0mm,e=0.5
	SWM34SRET6-50	3.0~3.6	140	512	64	8	51	5	11	1	1	1	18	1	1	1	4	2	2	2	2	1	1	1	1	2(16)	1	3/1	LQFP64	10.0*10.0mm,e=0.5
	SWM34SMEU6-40	3.0~3.6	120	512	64	2	60	5	12	1	1	1	20	1	1	1	4	2	2	2	2	1	1	1	1	2(12)	1	1/3 ¹	QFN80	9.0*9.0mm,e=0.4
	SWM34SVET6-50	3.0~3.6	140	512	64	8/16 ²	72	5	12	1	1	1	20	1	1	1	4	2	2	2	2	1	1	1	1	2(15)	1	4/3	LQFP100	14.0*14.0mm,e=0.5

Series	Part Number	Supply Voltage(V)	Max Frequency (MHz)	Memory(KBytes)		Stacked PSRAM (Mbytes)	I/O	Timer					Digital				Connectivity								Analog Interface			Package	Chip Size
				Flash	SRAM			Super	Base	SysClk	WDT	RTC	PWM	DVP	CRC	UART	I2C	SPI	I2S	CAN	TFT LCD	SDIO	QSPI	SRA	SAR ADC	DAC	VBAT		
SWM330	SWM330CGT7-50	2.0~3.6	140	1024	96	0	41	2	4	1	1	1	8	1	1	5	2	1	1	1	1	1	2	1	1(12)	1	1	LQFP48	7.0*7.0mm,e=0.5
	SWM330RGU7-35	2.0~3.6	140	1024	96	0	57	2	4	1	1	1	8	1	1	5	2	1	1	1	1	1	2	1	1(12)	1	1	QFN68	7.0*7.0mm,e=0.35
	SWM33SRGU6-35	2.0~3.6	140	1024	96	8	57	2	4	1	1	1	8	1	1	5	2	1	1	1	1	1	2	1	1(12)	1	1	QFN68	7.0*7.0mm,e=0.35

注1：CMP1/2反向端连接内部VREF
注2：SWM34SVET6内部叠封的SDRAM为8M、16Mbytes可选
注3：PWM包含X和Xn
注4：CMP0/1/2/3反向端连接内部VREF
注5：其中一路支持LIN功能
注6：其中一路为QSPI
注7：ADC0_CH3、ADC0_CH4、ADC0_CH9、ADC1_CH4、ADC1_CH5均为内部ADC通道，不占用外部ADC通道数量