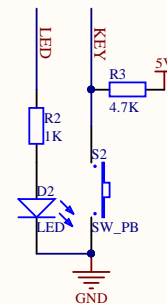
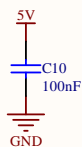
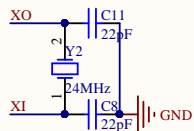


统一顺序: VDD GND SWC SWD



P2 Header-Female-2.54\_1x24

24	1
23	2
22	3
21	4
20	5
19	6
18	7
17	8
16	9
15	10
14	11
13	12
12	13
11	14
10	15
9	16
8	17
7	18
6	19
5	20
4	21
3	22
2	23
1	24

U3

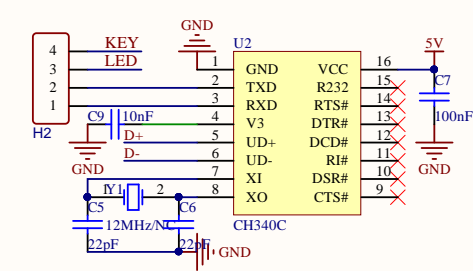
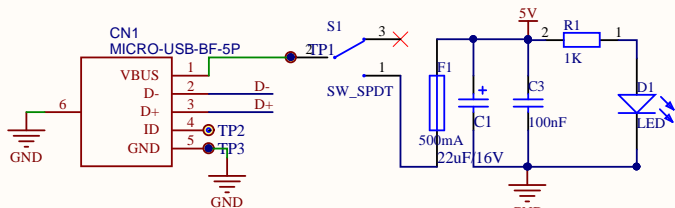
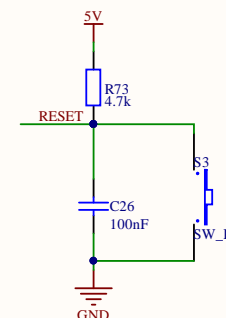
B15/UART0RX/SPIOSSN
M0/SWCLK/UART1TX/PWMPULSE0/STOPWAKEUP
M1/SWDIO/UART1RX/T11/T10
M2/PWM1BN/PWM1B/HALL0
M3/I2CCLK/UART0TX/PWM1B/PWM0A/PWM1BN/HALL1
M4/I2CODAT/UART0RX/PWM1AN/PWM1A/HALL2
M5/PWM1A/PWM1BN/PWM1AN
M6/PWM0AN/PWM0A
M7/PWM0A/PWM1AN/PWM0AN
M8/SPI0CLK/PWM0BN/T0I/T0O/WAKEUP0/ADC0_CH11
M9/QEIUPDN/SPI0MOSI/PWM0B/T11/T10/ADC0_CH10
M10/SPI0SSN/WAKEUP1
B0/UART0RTS/CMPVN2
B1/UART0CTS/CMPVN1
VDDIO
B2/UART0TX/CMPVN0
B3/UART0RX/SARADC_CH2
B4/QEA/I2C0CLK/HALL0/ADC0_CH6/CMPVP2
B5/QEB/I2C0DAT/HALL1/T11/T10/ADC0_CH5/CMPVP1
B6/INDEX/PWMBRK1/HALL2/T0I/T0O/ADC0_CH4/CMPVP0
B7/UART1TX/T11/T10/ADC0_CH9/OPVP0
B8/UART1RX/OPVN0
B9/UART1RTS/ADC0_CH8/OPOUT0
A8/UART1CTS/ADC0_CH7/OPOUT1

SWM201C6T7

B14/UART0TX/SPI0MISO/PWMBRK0/CMPVN3
B13/PWM0A
B12/UART0RX/SPI0MOSI/PWM0B/T0I/T0O/XO
B11/UART0TX/SPI0CLK/PWM0BN/T11/T10/XI
B10/PWM0AN/T0I/T0O/ISP
A7/I2CODAT/PWM0BN
A6/I2C0CLK/PWM0B
RESETn
40 5V
VDD5
CAP
VSS5
A5/UART1RX/PWM1A/PWM0AN/PWM1AN
A4/UART1TX/PWM1B/PWM1BN
A3/PWM0A/PWM1AN/PWM0AN
A2/PWM1AN/PWM0AN/PWM1A
A1/I2CODAT/UART0TX/PWM1BN/PWM1B
A0/I2C0CLK/UART0RX/PWM0AN/PWM1AN/PWM0A
A15
A14/T0I/T0O/ADC0_CH0/CMPVP3
A13/ADC0_CH1/OPVN1
A12/OPVN2
A11/PWMPULSE1/ADC0_CH3/OPOUT2
A10/PWMBRK2/OPVP2
A9/OPVP1

P1 Header-Female-2.54\_1x24

48	1
47	2
46	3
45	4
44	5
43	6
42	7
41	8
40	9
39	10
38	11
37	12
36	13
35	14
34	15
33	16
32	17
31	18
30	19
29	20
28	21
27	22
26	23
25	24



Synwit\_Logo  
SYNWIT\_CODE1

SWM201C6T7最下系统板V2.0

